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Visual Design and Reasoning with the Use of Hypergraph Transformations

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Abstract: This paper deals with visual design and reasoning. A visual language with its internal representation in the form of attributed hierarchical hypergraphs is discussed. Hypergraph attributes allow for defining and analysing constraints imposed by design knowledge. Operations on hypergraphs which reflect modifications of design diagrams are also presented. The approach is illustrated by examples of designing floor-layouts.

Keywords: visual design, graph transformations, hypergraphs, knowledge-based reasoning

1 Introduction

This paper describes a knowledge-based decision support design system where designs are configurations of visual elements. Both partial and complete design solutions are represented in the form of diagrams forming a specific visual language, called the layout language. The syntactic knowledge of this language is defined by means of attributed hierarchical hypergraphs.

The proposed approach constitutes an attempt to solve the problem of transforming the visual design knowledge into a computer internal representation for a system which is to support the designer in the early stage of the design process [GGLŁŚ, GŚ]. The compatibility between the proposed visual language and the internal representation that serves as a base for reasoning about designs is discussed.

In our approach each diagram drawn by the designer is represented in the form of an attributed hierarchical hypergraph. Hyperedges of hypergraphs represent both diagram components and the multi-argument relations among them. Hierarchical hyperedges correspond to groups of diagram components. Hypergraphs nested in these hyperedges correspond to subcomponents of the diagram parts. Hierarchical hypergraphs not only reflect the top-down way of designing but also enable the designer to consider the project on the specified level of detail and allow one to express relations between components on different hierarchy levels. Attributes assigned to hyperedges encode the semantic design knowledge.

Diagram modifications made by the user are reflected in the hypergraph representation, which is correspondingly changed using operations defined on hypergraphs. When the designer decides
to divide a chosen area of the layout into smaller parts the operation called hyperedge development [GŠG], is used. A reverse operation, called hyperedge suppression, which allows one to redesign the chosen area, is defined in this paper.

The paper strongly recommends the designer to interact with the computer system on the level of a visual language. The knowledge stored in the attributed hypergraph representation of diagrams allows the system to reason about layouts and to support the designer by suggesting further steps and prevent him/her from creating designs not compatible with the specified constraints or criteria. The approach is illustrated by examples of designing floor-layouts.

2 Related work

This paper deals with supporting conceptual design phase by a knowledge-based system. Nowadays detailed design and design documentation phases are usually well supported in CAD tools such as ArchiCAD, Architectural Desktop and AllPlan, etc. [Szu]. Although there are many computational tools for describing, editing, analyzing, and evaluating design projects [Min], there still exists lack of consistency between knowledge visualization of the given domain (architecture, construction, machine building) and its internal representation in a computer program. This is one of the reasons that the initial design phase, called conceptual design, is the least supported one.

Our approach proposes a visual representation of designs together with their internal representation. Such a method allows one to equip the design editor with intelligent assistants supporting creative design by reasoning about the project features and suggesting design modifications. When developing a visual language the role of sketches in the conceptual design phase was taken into consideration [Gol]. Therefore elements of our language contain general ideas about design objects and are not treated as completed design components used in design visualizations in all CAD tools.

Graphs and hierarchical structures are used quite frequently in knowledge-based design tools [SWZ]. Our approach is based on a formal model of hierarchical hypergraphs introduced in [DHP] and extended in [Slu]. This type of hypergraphs enables us to express multi-argument relations between elements on different hierarchy levels, which is essential in design. Our hypergraphs can be treated also as an extension of hypergraphs used by [Min], which are too restrictive in expressing relations.

3 Design Diagrams and Attributed Hierarchical Hypergraphs

In this section we present an example of a visual language, called a layout language, which enables the designer to create and edit floor-layouts. A vocabulary of this language is composed of shapes corresponding to components like rooms, walls, doors, windows, while the rules specifying possible arrangements of these components constitute its syntactic knowledge. Elements of the layout language are diagrams seen as simplified architectural drawings.

Let us consider a floor-layout presented in Figure 1(a) and its design diagram shown in Figure 1(b). A design diagram is composed of polygons which are placed in an orthogonal grid. These polygons represent components of a floor-layout, like functional areas or rooms. Mutual
location of polygons is determined by design criteria. Lines with small squares on them represent the accessibility relation among components, while continuous lines shared by polygons denote the adjacency relations between them. The accessibility relation between two areas is specified when the existence of a wall with doors, a fragment of a wall or lack of a wall are planned in a floor-layout design. When a wall dividing two areas is not planned, the location of a line representing the accessibility relation is determined by the specified sizes of the areas. The sides of each polygon are ordered clock-wise starting from the top left-most one. In a design diagram only qualitative coordinates are used i.e., only relations among graphical elements (walls) are essential.

Each design diagram has its internal representation in the form of an attributed hierarchical layout hypergraph. Such a hypergraph contains two types of hyperedges, which can represent components and spatial relations on different levels of details. Hyperedges of the first type are non-directed and correspond to layout components. Hyperedges of the second type represent relations among components and can be either directed or non-directed in the case of symmetrical relations. Considering floor-layout design only spatial relations which are by nature symmetrical (accessibility and adjacency) are taken into account.

An example of the internal representation of the diagram presented in Figure 1(b) is shown in Figure 2. This hypergraph is composed of eleven component hyperedges, three of which are hierarchical ones, and fourteen relational hyperedges, where half of them represent the accessibility relation and the other half the adjacency relation. The relational hyperedge connecting node 2.2 of the hyperedge labelled $L_r$ and node 4.6 of the hyperedge labelled $Be$ is one of the hyperedges expressing relations between components nested in different parent hyperedges.

To represent features of layout components and relations between them attributing of nodes and hyperedges is used. Attributes represent properties (like shape, size, position, number of
windows or doors) of elements corresponding to hyperedges and nodes.

The proposed hierarchical layout hypergraph constitutes a modification of hypergraphs presented in [Min, Slu] and is defined as follows.

Let $[i]$ denote the interval $[1, i]$ of natural numbers (with $[0] = \emptyset$) and let $\Sigma = \Sigma_E \cup \Sigma_V$, where $\Sigma_E \cap \Sigma_V = \emptyset$, be a fixed alphabet of hyperedge and node labels, respectively. Let $At$ be a set of hyperedge and node attributes.

**Definition 1** An attributed hierarchical layout hypergraph over $\Sigma = \Sigma_E \cup \Sigma_V$ and $At$ is a system $G = (E_G, V_G, s_G, t_G, lb_G, att_G, ext_G, ch_G)$, where:

1. $E_G = E_G^C \cup E_G^R$ is a nonempty finite set of hyperedges, where elements of $E_G^C$ represent object components, while elements of $E_G^R$ represent relations and $E_G^C \cap E_G^R = \emptyset$,
2. $V_G$ is a nonempty finite set of nodes,
3. $s_G : E_G \rightarrow (V_G)^*$ and $t_G : E_G \rightarrow (V_G)^*$ are two mappings assigning to hyperedges sequences of source and target nodes respectively, in such a way that $\forall e \in E_G^C s_G(e) = t_G(e)$,
4. $lb_G = lb_{E_G} \cup lb_{V_G}$, where:
   - $lb_{E_G} : E_G \rightarrow \Sigma_E$ is a hyperedge labelling function, such that $\Sigma_E = \Sigma_E^C \cup \Sigma_E^R \land \Sigma_E^C \cap \Sigma_E^R = \emptyset \land \forall e \in E_G^C lb_{E_G}(e) \in \Sigma_E^C \land \forall e \in E_G^R lb_{E_G}(e) \in \Sigma_E^R$,
V is a node labelling function,

5. \( \text{att}_G = \text{att}_{E_G} \cup \text{att}_{V_G} \), where:

- \( \text{att}_{E_G} : E_G \rightarrow P(\text{At}) \) is a hyperedge attributing function,
- \( \text{att}_{V_G} : V_G \rightarrow P(\text{At}) \) is a node attributing function,

6. \( \text{ext}_G : [n] \rightarrow V_G \) is a mapping specifying a sequence of hypergraph external nodes,

7. \( \text{ch}_G : E_C \rightarrow P(A) \) is a child nesting function, where \( A = E_G \cup V_G \) is called a set of hypergraph atoms, and such that one atom cannot be nested in two different hyperedges, a hyperedge cannot be its own child, source and target nodes of a nested hyperedge \( e \) are nested in the same hyperedge as \( e \).

Hyperedges of the layout hypergraph are labelled by names of components or relations. A sequence of source and target nodes is assigned to each hyperedge and express potential connections to other hyperedges. Moreover, for each hierarchical hypergraph a sequence of external nodes is determined. A child nesting function ensures that one atom cannot be embedded in two different hyperedges. It also guarantees that there are no ancestor-descendant cycles in a hypergraph, which means that a hyperedge cannot be its own child.

4 Operations on Hierarchical Layout Hypergraphs

It is known that a design process cannot be a priori defined in an algorithmic way. During a design process the designer often modifies a design diagram and/or changes design goals before he gets a plausible solution. To reflect these changes in our internal representation of a visual language we equip the proposed system with operations acting on hierarchical layout hypergraphs. They allow the system to create and modify hypergraphs representing structures of objects being designed.

The hyperedge development operation, which enables to represent a model structure on a more detailed level, was defined in \([GSG]\). It takes two hierarchical hypergraphs as arguments and nests in a hyperedge corresponding to an object element in the first hypergraph the second hypergraph representing the components of this element and relations among them. The nodes connected to a developed hyperedge are substituted by the corresponding external nodes of the child hypergraph.

Now we define in a formal way an operation called a hyperedge suppression, which is a reverse to a development operation. It is useful when the designer wants to redesign the chosen area. Then he/she has to remove the existing area division before dividing it in a different way. A suppression operation takes as an argument a hierarchical layout hypergraph and removes the nested contents of one of its earlier developed component hyperedges.

Let us consider Figure 2 and Figure 3. Figure 3 presents the result of applying a suppression operation consisting in removing a hypergraph \( H \) nested in the hyperedge \( \hat{e} \) labelled \( L \) corresponding to the living area (Figure 2).

Let \( S_G(e) \) and \( T_G(e) \) denote sets of all nodes specified by sequences of source and target nodes of a hyperedge \( e \) in a given hierarchical layout hypergraph \( G \).
Definition 2  Let $G$ be an attributed hierarchical layout hypergraph over $\Sigma = \Sigma_E \cup \Sigma_V$ and $At$, and $\bar{e} \in E^E_G$ be a component hyperedge of $G$ such that there exists a hierarchical layout hypergraph $H$ over $\Sigma$ and $At$, and $ch_G(\bar{e}) = E_H \cup V_H$, where $ch_G$ denotes the transitive closure of $ch_G$.

Let $EXT_H$ denote a set of external nodes of $H$, $V$ denote a finite set of nodes such that $V \cap V_G = \emptyset$, $E$ denote a set of all relational hyperedges of $E^E_G \setminus E^E_H$ and connected with nodes of $EXT_H$, and $E'$ denote a finite set of relational hyperedges such that $E' \cap E_G = \emptyset$.

Let a function $\text{substr}(string_1, string_2)$ return a substring of $string_1$ without the prefix $string_2$.

The hyperedge suppression operation is defined by three functions:

1. a suppression function $\text{sup} : EXT_H \to V$ defined in such a way that
   \[
   \forall v, w \in EXT_H \text{ substr}(|lb_G(v)|, |lb_H(v)|) = substr(|lb_G(w)|, |lb_H(w)|) \Rightarrow sup(v) = sup(w),
   \]
   is a surjection which determines the correspondence between a set of new nodes and external nodes of $H$ assigning one node to all nodes with labels which differ only by a prefix number.

2. a suppression labelling function $\text{lb}_{\text{sup}} : V \to \Sigma_V$ defined in such a way that
   \[
   \forall v \in V \text{ lb}_{\text{sup}}(v) = \text{substr}(|lb_G(v)|, |lb_H(v)|),
   \]
   is a mapping assigning to new nodes labels obtained by removing prefixes from labels of the corresponding external nodes of $H$.

3. a suppression embedding function $\text{emb}_{\text{sup}} : E \to E'$ is a surjection which determines the correspondence between a set of new relational hyperedges and relational hyperedges connected with external nodes of $H$.

The result of the hyperedge suppression operation is an attributed hierarchical layout hypergraph $G = (E^E_G, V^E_G, t^E_G, lb^E_G, \text{att}_G, ext_G, ch_G)$ over $\Sigma$ and $At$, where:

1. $E^E_G = (E^E_G \setminus E^E_H) \cup E'$
2. $V^E_G = (V^E_G \setminus V_H) \cup V$
3. $s^E_G : E^E_G \to (V^E_G)^*$ and $t^E_G : E^E_G \to (V^E_G)^*$ are defined in such a way that:
   - $s^E_G(\bar{e}) = t^E_G(\bar{e}) \subseteq \hat{V}^*$, where $\hat{V} = S^E_G(\bar{e}) \cup V$,
   - $\forall e \in E^E_G \setminus E^E_H \setminus \{\bar{e}\} s^E_G(e) = s^E_G(e) \cap t^E_G(e) = t^E_G(e)$,
   - $\forall e ' s^E_G(e ') = f(e ') \cap t^E_G(e ') = f(e ')$ where $f : E' \to (V^E_G)^*$,
4. $lb^E_G = lb^E_G \cup lb^E_G$, where:
   - $\forall e \in E^E_G \setminus E^E_H \setminus E lb^E_G(e) = lb^E_G(e)$,
   - $\forall e_2 \in E' lb^E_G(e_2) = lb^E_G(e_1)$, where $e_1 \in \text{emb}^{-1}_{\text{sup}}(e_2)$,
   - $\forall v \in V^E_G \setminus V_H lb^E_G(v) = lb^E_G(v)$,
   - $\forall v \in V lb^E_G(v) = lb^E_G(v)$,
5. $\text{att}_G = \text{att}_{E^E_G} \cup \text{att}_{V^E_G}$, where:
   - $\forall e \in E^E_G \setminus E^E_H \setminus E \text{att}_G(e) = \text{att}_G(e)$,
\[ \forall e_2 \in E' \; att_G(e_2) = att_G(e_1), \text{ where } e_1 \in \text{emb}_{\text{sup}}^{-1}(e_2), \]

\[ \forall v \in V_G \setminus V_H \; att_G(v) = att_G(v), \]

\[ \forall v \in V \; att_G(v) = h(v), \text{ where } h : V \rightarrow P(At), \]

6. \[ \text{ext}_G : [\tilde{n}] \rightarrow V_G \times \Sigma_V \]

7. \[ \forall e \in E_G \setminus \{\tilde{e}\} \; ch_G(e) = ch_G(e) \land ch_G^+(\tilde{e}) = \emptyset. \]

As the result of the hyperedge suppression operation the hypergraph \( H \) is removed from the component hyperedge \( \tilde{e} \) of \( G \). The external nodes of \( H \) are substituted by the corresponding nodes of \( V \) which become new source nodes of the hyperedge \( \tilde{e} \). The correspondence among nodes is established by a suppression function \( \text{sup} \) on the basis of the node labels. The source nodes of \( \tilde{e} \) have the same labels as before a development operation on \( \tilde{e} \).

The suppression embedding function replaces the relational hyperedges connected to the external nodes of \( H \) by new relational hyperedges connecting nodes of \( G \) to source nodes of \( \tilde{e} \). The way of replacing each hyperedge by the corresponding set of new hyperedges is specified on the basis of design constraints.

Let us come back to the example of the suppression operation removing a hypergraph \( H \) nested in the hyperedge \( \tilde{e} \) labelled \( L \) corresponding to the living area (Figure 2 and Figure 3). Node replacing specified by the function \( \text{sup} \) determines six new nodes (a set \( V \)) corresponding to ten external nodes of the nested hypergraph. The labels of new nodes are obtained by removing prefixes coming from the corresponding external nodes of the removed hypergraph. For example, two nodes with labels 2.4 representing east walls of the hall and toilet, respectively, are merged into one node with label 4 representing the north side of the area \( L \), while three nodes labelled 3.5.3 are merged into one node labelled 5.3 and representing the south wall of the living area.

The four relational hyperedges which were connected with nodes 2.2, 1.3, 2.4 and 2.4 (\( E = 2.2, 1.3, 2.4, 2.4 \)) are replaced by three new relational hyperedges of \( E' \) connected with nodes 2, 3 and 4 of the hyperedge labelled \( L \) according to the suppression embedding function. As the result of this suppression operation a hierarchical layout hypergraph shown in Figure 3 is obtained.

5 Visual reasoning

Visual languages play a similar role in design as design sketches as they enable to follow changes made in design diagrams. Nowadays computer systems with visual languages should be equipped with intelligent tools capable of assisting the user in a computational process. Such tools ought to be able to reason on the basis of the internal representations of visual language elements.

The presented system is able to reason about the diagram being designed and suggest the designer modifications which are needed and warn him/her against creating solutions not compatible with the specified constraints. To this end the system is equipped with a set \( Sr \) of predicates of the form \( r : P(\mathcal{H}) \times \mathcal{H} \rightarrow \{\text{TRUE, FALSE}\} \), where \( \mathcal{H} \) denotes a family of attributed hierarchical layout hypergraphs over \( \Sigma \) and \( At \), and \( \mathcal{H} \) denotes design knowledge. Each predicate
tests whether the predefined criteria are satisfied for the presently generated hierarchical layout hypergraph.

Design knowledge is divided into syntactic knowledge $K_s$ and interpretation knowledge $K_i$ [CRRBG]. The predicates of $Sr$ are divided into syntactic predicates ($Sr$) based on syntactic knowledge and semantic ones ($Sm$) based on interpretation knowledge. Each predicate of $Sr$ has a specified subhypergraph which is searched for in a layout hypergraph created so far. After each hypergraph development operation, which nests a hypergraph in a hyperedge, for each component hyperedge of a nested hypergraph a predicate $r_1 \in Sr$, which tests the accessibility of the corresponding room or area, is activated. This predicate searches for a subhypergraph shown in Figure 4(a), where $Lab$ denotes a label of a component hyperedge. For example in a hypergraph nested in a hyperedge representing the whole apartment (Figure 2) and presented in Figure 8(b) for both component hyperedges labelled $S$ and $L$ one such subhypergraph is found, while for a hyperedge labelled $G$ representing a garage a searched subhypergraph is not found. The designer is notified that there is no inside connection of a garage and it is accessible only from the outside of the house.

The example syntactic predicate $r_2$ tests the direct accessibility between two chosen areas or rooms. It searches in a hypergraph for a subhypergraph of the from presented in Figure 4(b). Finding a subgraph isomorphic with the one shown in Figure 4(b) (labels $Dr$ and $K$ denote a dining-room and a kitchen, respectively) means that a dining-room is located near a kitchen and accessible from it. If these conditions are not satisfied for the existing dining-room the designer obtains a suitable piece of information. As the result of testing the direct accessibility between
Figure 4: a) A subhypergraph of a predicate $r_1$, b) a subhypergraph of a predicate $r_2$

Figure 5: Testing the direct accessibility between two chosen areas

a living area and a bedroom, which is shown in Figure 5, both areas are marked using white squares.

Predicates belonging to a set $Sm$ enable the system to perform semantic analysis of the generated hypergraph. The semantic reasoning about a design diagram is performed on the basis of identifiers and present values of attributes assigned to hypergraph atoms. To each component hyperedge the attribute $area$, whose value specifies the area of the corresponding space, is assigned. To relational hyperedges labelled $acc$ the attribute $type$ is assigned. Its value specifies the way in which the adjacent spaces are accessible (by the door, by lack of a fragment or a whole wall between them). To hypergraph nodes the attributes $position$ and $window\_number$ are assigned. They specify the location of the walls and the numbers of windows which the walls contain.

The example semantic predicate $r_3$, which is activated for each room located in a design diagram, sums up values of the $window\_number$ attribute of all nodes assigned to a corresponding component hyperedge representing this room. Comparison of the obtained number with the specified architectural norms allows the system to reason about the lighting of the room and the loss of heat, and warn the designer about the possible problems. Another example of semantic reasoning concerns deducing shape of rooms on the basis of identifiers of nodes assigned to hyperedges representing diagram components. First, a syntactic predicate, which searches for composition hyperedges connected with six or eight nodes, is activated. For the found hyperedges of the first type, a semantic predicate $r_4$ tests if the corresponding areas have the shape
of a letter \( L \), while for the hyperedges of the second type, a semantic predicate \( r_5 \) tests if the corresponding areas have the shape of a letter \( T \). Predicate \( r_4 \) searches for two pairs of nodes, each of them representing two non-collinear and parallel walls, and such that walls represented by these pairs are not perpendicular (hyperedges \( S \) and \( L \) in Figure 8(b)). Predicate \( r_5 \) searches for three nodes representing three parallel walls with the same orientation and such that at most two of them are collinear, one node representing a wall which is parallel to the mentioned three walls but has the opposite orientation. Two remaining pairs of nodes should represent parallel and non-collinear walls with opposite orientations.

6 Implementation

In this section a structure of the system HGSDR (Hypergraph Generator Supporting Design and Reasoning) is presented. The system allows the designer to edit diagrams and automatically applies operations on hierarchical layout hypergraphs being internal representations of diagrams. It also gives the possibility to define constraints and reason about design diagrams.

The system is written in Java and contains four modules (Figure 6): a graphical interface for editing objects and constraints, a constraint module for reasoning about diagrams, a hierarchical layout hypergraph generator and a control module for hypergraph visualization. The graphical interface enables the designer to construct diagrams of visual elements directly accessible in the editor or taken from a library of objects defined by the user or an external library of domain-oriented objects. A set of visual primitives, which are directly accessible, contains points, line segments, polygons and arcs. Texts can be used to describe primitives. The graphical interface allows the designer to interactively create and edit both objects, their attributes and constraints concerning these attributes.

The constraint module contains syntactic and semantic reasoning rules. It activates the rules,
solves constraint equations defined on hypergraph attributes and sends messages to the designer. It prompts the designer which diagram transformations are needed. When the designer changes values of attributes, which are connected by specified constraints (like distance or location), the system can force the appropriate constraints to be satisfied by changing sizes of some objects and drawing a new diagram again.

The hierarchical layout hypergraph generator automatically creates hypergraphs, where hyperedges correspond to diagram components and relations between them. The control module for hierarchical hypergraph visualization verifies and updates an existing hierarchy and enables to group components of the same hierarchy level and show relations between these groups and other components being on different levels of the hypergraph hierarchy. Navigation across different layers of a hypergraph is possible due to an auxiliary tree of hierarchy. Using this tree the designer chooses hyperedges which should be visualized and decides if their contents should be visible. In Figure 7 the rooms nested in the sleeping area are not chosen in the tree of hierarchy and therefore the contents of the hyperedge corresponding to the sleeping area is not visible in the hypergraph.

7 Case Study

Let us consider an example of creating a design diagram presented in Figure 1(b), which corresponds to the layout of a one storey house with a garage shown in Figure 1(a). The first diagram
Figure 8: a) A component hyperedge representing the whole area of the apartment, b) a hierarchical layout hypergraph obtained as a result of a hyperedge development operation.

The designer represents the area of the whole apartment. The initial hypergraph representing this diagram, that is automatically generated (Figure 8(a)), is composed of one hyperedge connected with four external nodes representing sides of the area and placed in the diagram according to the geographical location of the sides they correspond to.

In the next step, the designer divides the whole apartment area into three parts representing a living area, sleeping area, and a garage, respectively (Figure 9(a)). As a consequence, the hyperedge development operation on the layout hypergraph is invoked automatically. As the result of this operation the layout hypergraph representing the three areas and adjacency relations between them is nested in the hyperedge representing the whole apartment. The obtained hierarchical layout hypergraph is shown in Figure 8(b).

The four external nodes of the hyperedge shown in Figure 8(a) are replaced by seven external nodes of the nested layout hypergraph in respect to their geographical orientations. The labels of the external nodes of the layout hypergraph nested in the hyperedge representing the apartment are concatenated with labels denoting the number of the parent hyperedge node they replaced. The node number 1 of the apartment is replaced by nodes representing north sides of the living area (L) and the sleeping area (S). Both new nodes are labelled 1.1, where the first part of this label denotes that they correspond to first sides of areas L and S, while the second part of the label is inherited from the node which they replaced. The node number 2 is replaced by node 2 of the sleeping area and node number 2 of the garage, where both of them represent east sides of the diagram and are labelled 2.2. The node number 3 is replaced by node number 3 of the garage (labelled 3.3) and node number 5 of the living area (labelled 5.3). The node number 4 of the apartment is replaced by the node number 6 of the living area (labelled 6.4).

Then, the sleeping area is divided by the designer into a bedroom and a bathroom, which are
adjacent to each other (Figure 9(b)). This modification of the design diagram results in nesting
the layout hypergraph representing these rooms and adjacency between them in the hyperedge
labelled $S$ representing the sleeping area. As the result of applying the hyperedge development
operation the hierarchical layout hypergraph presented in Figure 3 is obtained.

Six external nodes of the hyperedge labelled $S$ are replaced by seven corresponding external
nodes of the nested hypergraph. The labels of these seven nodes are concatenated with labels de-
noting numbers of the parent hyperedge nodes they replaced. For example, the node labelled 1.1
of the sleeping area is replaced by nodes representing north walls of the bedroom ($Be$) and bath-
room ($Ba$) (nodes 1.1.1 and 1.1.1, respectively). The relational hyperedges which were connected
with nodes 3, 4, 5 and 6 of the hyperedge labelled $S$ are replaced by new relational hyperedges
connected with nodes 3.3, 2.4, 3.5, and 4.6 of the nested hypergraph, respectively.

Then, the designer divides the living area into five rooms representing a living room, kitchen,
hall, entrance and a toilet, respectively (Figure 1(b)). As the result of the next hyperedge develop-
ment operation, which nests the layout hypergraph representing these five rooms and adjacency
relations between them in the hyperedge representing the living area the hierarchical layout hy-
pergraph shown in Figure 2 is obtained. Six external nodes of the hyperedge labelled $L$ are
replaced by the corresponding external nodes of the nested layout hypergraph. For example,
the node labelled 4 is replaced by two nodes 2.4 representing east walls of the hall and of the
toilet ($W$), respectively, while the node 5.3 by three nodes labelled 3.5.3 which correspond to
south walls of the kitchen ($K$), entrance ($E$) and toilet, respectively. Three relational hyperedges
which were connected with nodes 2, 3 and 4 of the hyperedge labelled $L$ are replaced by four
new relational hyperedges. For example, the hyperedge which was connected with node 4 of the
living area is replaced by two relational hyperedges, one connected with node 2.4 of the hall and

Figure 9: a) Three areas of the apartment, b) the apartment with the divided sleeping area
another one with node 2.4 of the hyperedge labelled $W$ (Figure 2).

If the designer is not satisfied with the layout of the living area and decides to change it, then she/he removes the division of this area and goes back to the diagram shown in Figure 9(b). As a consequence, the hyperedge suppression operation on the layout hypergraph is invoked automatically. As the result of this operation the hierarchical layout hypergraph shown in Figure 3 is obtained. The nodes which were divided by the development operation are merged again. Node replacing is specified in such a way that the merged nodes are given the same labels which they had before the development operation was used.

In the next step the living area is divided into four spaces corresponding to a living-room, kitchen, entrance and a toilet. The design diagram obtained as a result of a new division of $L$ and the corresponding hierarchical layout hypergraph are presented in Figure 10(a) and Figure 10(b).

8 Conclusions

This paper is the next step in developing a visual language to support innovative design. In our approach the designer’s modifications of diagrams are reflected by operations performed on their hypergraph representations. The way of reasoning about design diagrams on the syntactic and semantic level, based on knowledge stored in attributed hierarchical layout hypergraphs, that enables the system to suggest the designer modifications of created solutions, is also described.

The presented system is tested on designing floor-layouts. Other applications will concern visual languages for designing gardens in different styles and designing three-dimensional forms of buildings. The present implementation is written in such a way that each new application requires only some changes in the editor module.
Bibliography


